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20277

7590

12/02/2003

MCDERMOTT WILL & EMERY
600 13TH STREET, N.W.
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| APPLICATION NO. | FILING DATE | FIRST NAMED INVENTOR | ATTORNEY DOCKET NO. | CONFIRMATION NO. |
|-----------------|-------------|----------------------|---------------------|------------------|
|-----------------|-------------|----------------------|---------------------|------------------|

09/964,480

09/28/2001

Keiichi Fujimoto

740819-035

2785

TITLE OF INVENTION: SEMICONDUCTOR INTEGRATED CIRCUIT TESTING SYSTEM AND METHOD

60188-448

| APPLN. TYPE | SMALL ENTITY | ISSUE FEE | PUBLICATION FEE | TOTAL FEE(S) DUE | DATE DUE |
|-------------|--------------|-----------|-----------------|------------------|----------|
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nonprovisional

NO

\$1330

\$300

\$1630

03/02/2004

| EXAMINER | ART UNIT | CLASS-SUBCLASS |
|----------|----------|----------------|
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PATEL, PARESH H

2829

324-765000

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1. McDermott, Will & Emery

2. _____

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MATSUSHITA ELECTRIC INDUSTRIAL CO., LTD.

Osaka, JP

Please check the appropriate assignee category or categories (will not be printed on the patent);

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(Authorized Signature)

(Date)

Michael J. Fogarty, Reg. #36,139 2/26/04

03/02/2004 DEMAND 00000133 500417 09964480

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